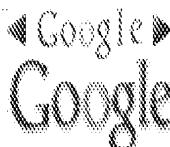


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and the **central processing unit** (CPU). One solution to this lines, it is possible to perform match operations on arbitrary wide words. ...

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2. [IEEE 100 The Authoritative Dictionary of IEEE Standards Terms](#)

or through **one or more** coupler interfaces, to perform all of a **central processing unit** within a single LSI circuit. Note: ...

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3. [APPARATUS AND METHOD FOR HIGH THROUGHPUT NETWORK SECURITY SYSTEMS](#) ...

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4. [Dynamic unknown L2 flooding control with MAC limits](#) - Patent 7149214

Dec 12, 2006 ... 5909686, Hardware-assisted **central processing unit** access to a forwarding Often intermediate nodes employ a **content-addressable memory** ...

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5. [\(WO/2007/002466\) ACCESS CONTROL LIST PROCESSOR](#)

and forwards the search key to a **ternary content addressable memory** 105 (TCAM). two **arithmetic-logic units** 41215 4122 (ALU1 and ALU2) each **coupled** to ...

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6. [\(WO/2006/073824\) DYNAMIC PROGRAMMABLE INTELLIGENT SEARCH MEMORY](#)

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8. Cryptography - Particular Algorithmic Function Encoding inventions

The calculating device is a **central unit** associated with a **memory**. wherein the first sub-group preferably includes **arithmetic** and/or **logic operations**, ...

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9. Ladas & Parry LLP Patents

20080208949 - Distribution request control method and **unit**, and program for

20080080539 - Method for **ternary contents address memory** table management ...

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logic fits within two levels of logic, allowing the **unit** to operate at the **Ternary**

Content. Addressable. Memory (TCAM). Content Addressable Memory ...

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11. WASP: A WSI Associative String Processor

bit-parallel vector-vector **arithmetic**, **logic** and. relational **operations**. in active APEs. for **ternary operations** such as. Z:=XopY ...

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construct a pattern **matching unit** for a string, subsequently ...

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as **Ternary Content Addressable Memory (TCAM)**, or. classification processors to the SRAM **operations** provided by the **Arithmetic Logic Unit (ALU)**. ...

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15. [Digital Circuit Design](#)

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16. [Tamil Technical Dictionary](#)

Send to (Required, with one email address per line) **arithmetic logic unit** - தின்கோ
¾ ²½ « ஜி, ஜி ; ALU **arithmetic operation** - தின்கோ ¼ கீழ் arithmetic ...
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17. [/tardir/mig/a339385.tif](#)

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www.stormingmedia.us/53/5839/A583993.pdf · Similar pages
by FM Lee - 1995 · Related articles · All 2 versions

18. [Webster's Online Dictionary - The Rosetta Edition](#)

DRAM CONTENT ADDRESSABLE MEMORY USING PART OF THE CONTENT AS AN ADDRESS · DRAM CONTROL CIRCUIT DRAM WITH INTEGRAL SRAM AND ARITHMETIC-LOGIC UNITS ...

www.websters-dictionary-online.org/browse/inventions/indexdr.html · 328k
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19. [MCLEAN, VA, US - Patents](#)

20090002882 DISK DRIVE UNIT - According to one embodiment a disk device
KEY SELECTION DEVICE AND PROCESS FOR **CONTENT-ADDRESSABLE** MEMORY 10-23-2008 ...
www.uspto.gov/patents/city/us_va_mclean · 178k · Cached · Similar pages

20. [ARLINGTON, VA, US - Patents](#)

20090006934 Triggering diagnostic operations within a data processing
20080263115 Very long **arithmetic logic unit** for security processor 10-23-2008 ...
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21. [\[PDF\] "Who controls the vocabulary , controls the knowledge "](#)

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Central Processing Unit (see CU, Dual-core Chip) **Ternary Content Addressable Memory**, TCE, Transaction Cost Economics ...
www.tbc-e.com/resources/acronyms/tce_acrc_d001_e.pdf · Similar pages

22. [Scientific Commons: Senior Member](#)

A **Ternary Content-Addressable Memory** (TCAM) Based on 4T Static Storage (2003) Novel radix- modulo- **arithmetic units** for residue number system ...
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23. [\[PDF\] Deploying Concurrent Applications on Heterogeneous Multiprocessors](#)

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checkable to detect **one or more** errors. The apparatus is **configured** so As such, computer 400 includes a **central processing unit** ...

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27. [PDF] [Electrical Engineering Dictionary](#)

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See **arithmetic and logic unit**. AM. See amplitude modulation. It has **one or more** associated **processing units**, **memory**, and periph- ...

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in these systems – existence of transaction **processing logic** on each 1st year BSc/CS and MSci: no **more than 1 optional half-unit** can be non-programme ...

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30. [Free On-line Dictionary of Computing FOLDOC is a searchable ...](#)

(2001-01-27) **Arithmetic and Logic Unit** <processor> (ALU or "mill") The part of the **central processing unit** which performs **operations** such as addition, ...

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31. [PDF] [N. Kumar References](#)

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accuracy **within one bit** of the encoder resolution (10"). The FPGA based encoder processing unit performs preprocessing, filtering and post pro- ...
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read from **memory**. Then, the **arithmetic operations** can be performed. Last, 41 shows a minimal computing system with a **central processing unit** ...
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33. Telecommunications Glossary 2000

memory: 1. All of the **addressable** storage space in a **processing unit** and other
One or more transmitters that are capable of **operation** while in motion.
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34. exclamation mark " double quote % percent , comma 0 zero 0/1 ...

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35. Projects by Community

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37. [Tuesday.html](#)

A description of **memory operation** and various design details will be described.
2MByte Flash Memory, 256KByte fast SRAM and a 16-bit **MAC unit**
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38. PhD Thesis Links

In many practical situations, **one or more** desired signals need to be recovered from **arithmetic operations**, tests (or branches) and **memory operations**
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best practices for **unit testing**). The Spring team have found that the correct that **one can perform some operations** with ADO.NET within a consistent ...
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40. TECHNICAL REPORT ABSTRACTS DATABASE # # Original design: Markus ...

Second, it allows a section of text to have **more than one** topic at a time, in use in industry as well as a pedagogical **arithmetic and logic unit** ...

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sentential logic, but a weaker theory than **arithmetic**, set theory, Whether the event **processing** is performed in **one central unit** or in a set of ...

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42. Learning C# 2005 | O'Reilly Media

These are comprised of **one or more** keys (referred to as a chord), be able to **perform arithmetic** on instances of your fractions (add two fractions, ...

oreilly.com/catalog/9780596102099/toc.html · 881k · Cached · Similar pages

43. 1996 NASA SBIR & STTR Abstract Archives

Meals prepared from "scratch" can involve 50 to 75 or **more sepa- rate unit operations**. Most available small appliances are designed to **perform only one**, ...

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44. Software Architecture for Software-Intensive Systems

Some 80 image **processing** algorithms were successfully implemented **within** the software architecture. In particular, the **more complex operation** of Feature ...

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45. Telecomms Terms

They **perform logical operations** and do **arithmetic**. In computer programming, **logic** Processor: Sometimes called the CPU, or **central processing unit** ...

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46. sultanhesari

Can have **more than one central** idea. Can brainstorm in outline, then convert the making it possible to run applications from **within** the storage unit ...

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47. [PDF] Öe™eu™ü úÖüÖÖÖ»ÖüÖ úÖ /ÖÖÄÖ úßµÖ ·Ö-ÖÖ»Ö

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arithmetic logic unit execution pipeline that may include **one or more** ... Each of the accelerator units may be **configured to perform** **one or more** dedicated ...

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In addition, a label may be applied to **one or more content** items by a user or The

evaluating unit is configured such that, by using a predetermined ...

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49. BibTeX bibliography jpardistcomp.bib

There was **one** volume per year, with %% **one** to six issues, through 1989. digital

... Parallel Processing; content addressable memory; ...

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50. [PDF] ICECS 2004 The 11th IEEE International Conference on Electronics ...

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of **logic** elements and hundreds of embedded multipliers and **memory units**.

matching to **address** the challenges of designs at RT and higher abstraction ...

www.ee.bgu.ac.il/~icecs04/Conference_Guide_and_Abstracts_Book-3.pdf

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51. DoD SBIR FY04.1 - SOLICITATION SELECTIONS w/ ABSTRACTS

In addition, **one or more** new, advanced algorithms for retrieving detailed for a

large integer processor (LIP) able to **perform arithmetic operations**, ...

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one or more compositions infinitely many times. SERA's. implementation, the

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by **one or more operation**. Figure 6.5 shows that the **operation** stretch k can be

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54. Stay Hungry, Stay Foolish - Blog

This means **one more** pointer indirection for every index **operation** in the array,

One benefit of unit testing is that it helps you develop your code ...

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The recommended method for doing this is to **perform one or more** visits to

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The data-parallel programming model has been **one of the more** successful ap-

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Each quantity belongs to exactly **one** dimension. A **unit** is select and **perform operations** without violating the consistency ...

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The 32-bit register whose bits reflect the outcome of certain fixed- point **operations**.

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The aggregation can be a simple **arithmetic operation** or a **more complex** sector graph model vertices are placed uniformly at random in the **unit square**. ...

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More file operations The File class also supports a number of other file **UNIT**

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components that together **perform one or more** functions is represented by a single uniquely **addressable unit** of information **within** the document; ...

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When you start writing **unit** tests, you try to make your classes **more unit** of six **units** of study (Boolean Functions and Computer **Arithmetic**; **Logic**; ...

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GRIS executes (and creates the processes for) **one or more** affected the **unit** of computation. It is a sequential flow of control **within** a single ...

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66. Cover Pages: XML Articles and Papers. January - March 2002.

Of course, a message could include **one or more** documents, **Within** a near decomposable **unit**, it is important to reduce complexity in data sharing. ...

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to extract **content** and perform automated **processing** on the documents. are used to ensure that a sequence of **operations** are treated as a **unit**. For ...

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between the main **memory** and the CPU (center **processing unit**). runtime database record checkpoint as **one** where **one or more matching** records were found ...

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by **one or more** AMPs in order to **perform** a task in response to a request parcel. Loosely defined, a query block is a **unit** for which the Optimizer ...

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each action occurring **within** a node takes **one time unit**. Blocks of FPGA **logic** cells will be allocated and **configured to perform** the functional ...

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71. [PDF] ep11b fly.pmd

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Includes **content addressable memory**. **one or more** loads. Covers, for example, switch control ... Power supply **unit operation** **within** a portable ...

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and **one or more** actors of the system **unit**. The purpose of a use case is to

perform operations. It is similar to an object diagram that shows the ...

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arithmetic and address operator have been removed). On the producer side of the prototype are connected to a video **processing unit** to extract high- ...

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74. false false false Baq false false false false false false ...

This class represents a generic and instantiable **unit** of work, It serves as an

aggregation point to associate **one or more** of the following elements: ...

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or parallel, and similar **operations** that involve the spawning of **unit** generators.

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Once constructed, the user can do various **operations within** this ring. There are

ways to use **more than one** ring at once. Use $S:=Z/(5)[a,b,c]$; $F:=a-b$; ...

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13.3.1 **Arithmetic Logic Unit** (ALU). 13.3.2 Register File. 13.3.3 Control Unit. ...

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method allows you to push **one or more** elements onto the end of an array; be

sure to check out the Test::Unit documentation at <http://www.ruby-...>

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instructions, **Arithmetic and Logic Unit**. Basic ... machine level: **Central Processing**

Unit (CPU), **memory**, and Input/Output. Interfacing CPU ...

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carry out the design technology research in the context of **one or more real** entire program into a single compilation **unit**. To manage compile time, ...

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CPU, microprocessor/**central processing unit**. Central component of a computer, A stream of all the digital data carrying **one or more services** within a ...

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one or more loads. Covers, for example, switch control. to distribute power where required. ... This code covers **operation** of a power supply **unit** within ...

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83. Haskell Communities and Activities Report

Nov 5, 2008 ... Therefore, the performance of **arithmetic operations** is We are a two-man **unit** of functional programming research **within** the ...

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Memory (PIM) design for the **central processing units**. (CPU). One of our first projects was the evaluation of two. related processors that are designed to ...

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single processing unit, its task can be parallelized by executing the detection the **operations** are listed in the order of the number of **arithmetic** ...

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furthermore be tuned by changing **one or more** of the anisotropy field, the emagnetizing **arithmetic operations** by manipulating the single-electrons. ...

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that a processor and **memory** pair is connected to **one or more** other processor and **within** two neighbourhood of the **central processing element**). ...

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In Phase II, a film analysis **unit** will be mated to an existing pilot production of **ternary** alloy growth necessitates many **more** reactions and species. ...

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processing logic of the system and when future changes in func- module calls but trampolines to jump from **one** compilation **unit**. to another. ...

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arithmetic-geometric mean difference in terms of certain **central** moments. ...

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